

Optically Immersed 2.1 μm Photodiode

PD21Su, PD21Sr

TE cooled Optically Immersed 2.1 μm Photodiode

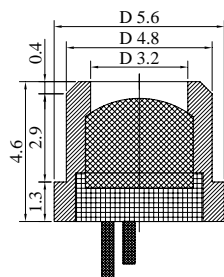
PD21TO8TEC

Peak wavelength	λ_{max}	μm	2.0÷2.1	@22 °C
Current sensitivity at λ_{max}	$S_I(\lambda_{\text{max}})$	A/W	≥ 0.6	
Shunt Resistance	R_0	kOhm	≥ 20	
Detectivity	$D^*_{\lambda_{\text{max}}}$	$\text{cmHz}^{1/2}\text{W}^{-1}$	$\geq 1.6 \times 10^{11}$	
Voltage sensitivity	S_U	V/W	$\geq 12\ 000$	
Switching time	τ	ns	≤ 20	

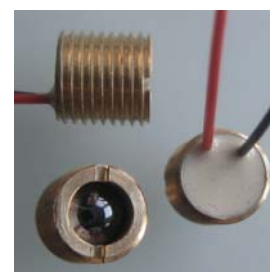
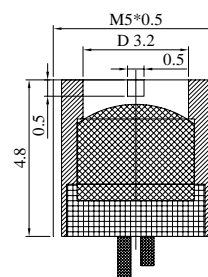
Code	Sensitive area, mm	Weight, g	Optical components	Field of view, deg.	Optical axis deviation, deg.	Detectivity deviation in lot, %	Operation conditions, °C	Lifetime, hrs
PD21Su PD21Sr	$\varnothing 3.2$	~0.4	Si lens	~15	≤ 5	± 25	-60÷+85 ²	>80 000
PD21 TO8TEC		~10	Si lens and output sapphire window D=6mm				-60÷+85 ³	

Product view

PD21Su

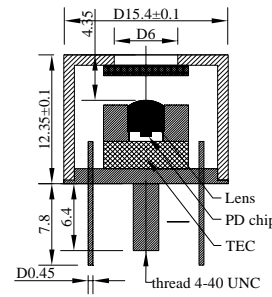
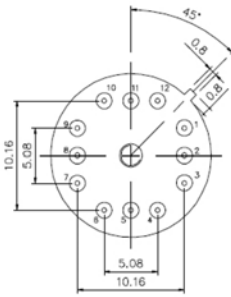
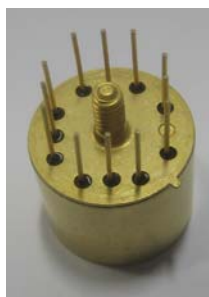


PD21Sr



Pin assignment: red wire or long wire and red point on house - positive

Pin assignment: red wire or long wire and red point on house - positive



Pin assignment
PD21TO8TEC12

- 1 TEC negative;
- 3 TEC positive;
- 4 PD negative;
- 6 PD positive;
- 7, 9 thermosensor;
- 11 \perp (House);
- 12 \perp (PD)

Features

- Original growth of narrow gap A3B5 semiconductor alloys onto n^+ -GaSb substrate;
- Flip-chip design of PDs;
- Optical coupling through the use of chalcogenide glasses and Si lenses with antireflection coating
- Ambient and high temperature operation;
- No bias required;
- Operation from DC to VHF;
- Highest long term stability;
- High value of shunt resistance;

Photodiode could be equipped with preamplifier that is designed for conversion of PD photocurrent into a convenient output voltage and is adjusted for the particular PD taking into account the R_0 value and frequency range. Other packages are available upon request. Angle of view is small and thus we recommend adjusting PD position regarding to the emission system before final evaluation/use of the devices. Data are valid for PD thermostabilized at 22°C. Heatsink is essential for TEC operation!

Notes

¹ - according to estimation

² - devices have passed through 15 thermo cycles : (20°C, 8 hrs) -transition period of 30 min - (+125°C, 8 hrs) without changes in specifications. Valid for devices produced since 01.2013

³ - devices have passed through 15 thermo cycles : (-60°C, 30 min) - transition period of 30 min -(+85°C, 30 min) without changes in specifications. Valid for devices produced since 01.2013

Product specifications are subject to change without prior notice due to improvements or other reasons. Updated 08.05.13

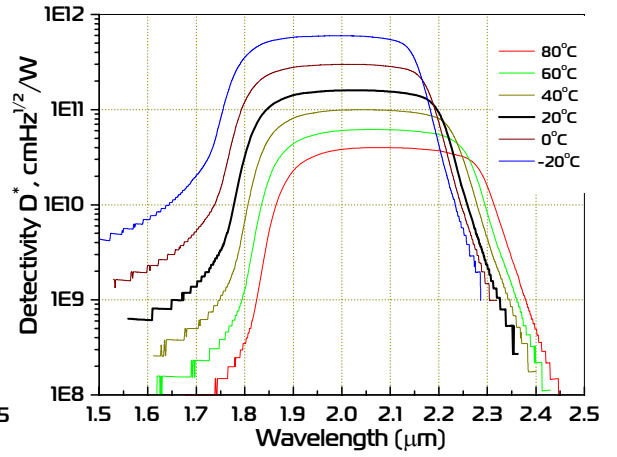
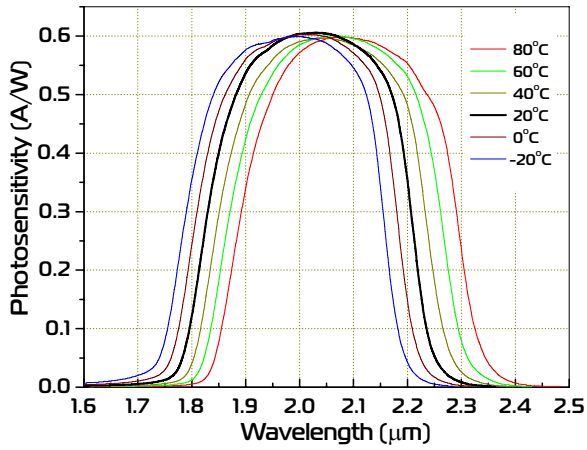
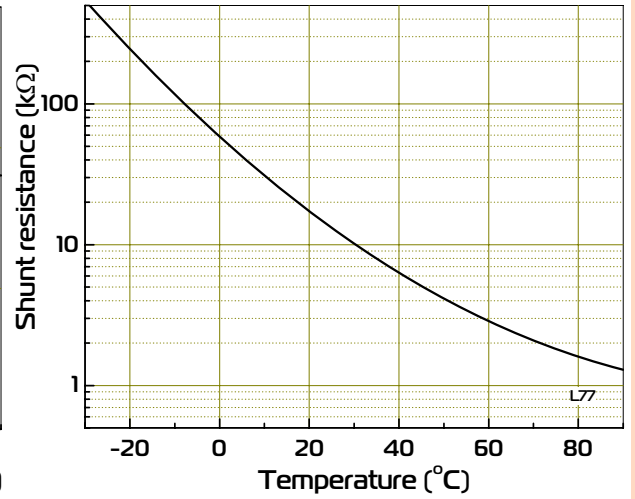
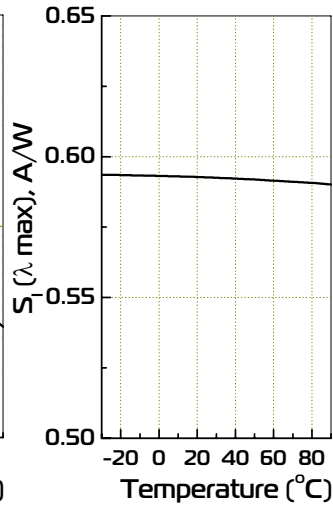
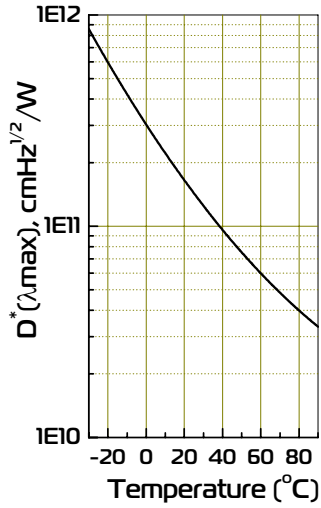


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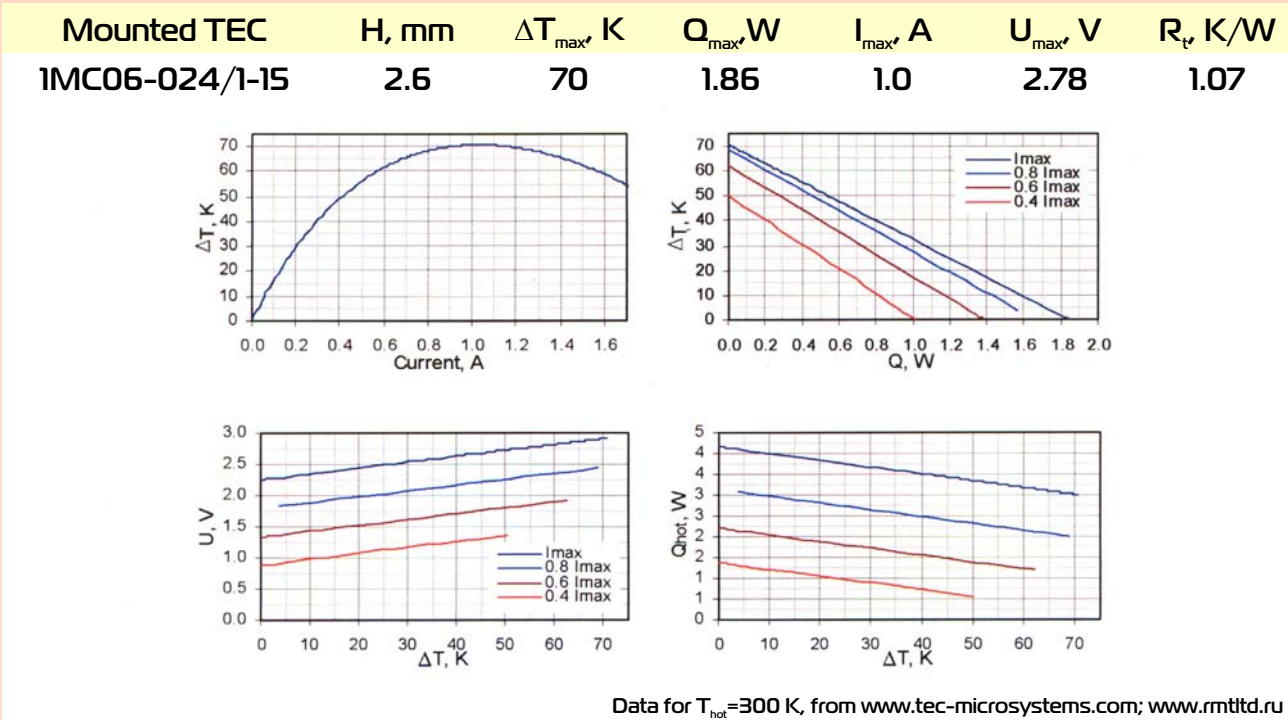
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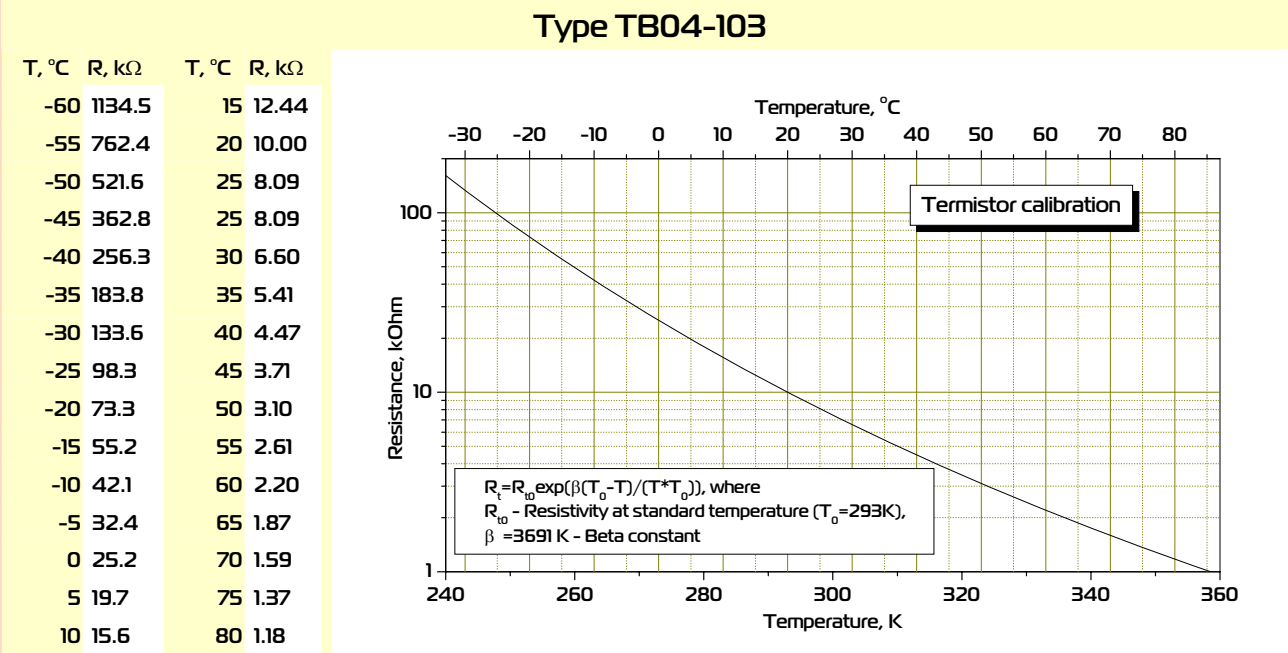
Spectral response

Detectivity, current sensitivity at λ_{max} and shunt resistance vs. temperature

Thermoelectric cooling module datasheet



Thermistor specification



Possible TEC heatsink view

